



ಐಐಟಿ ಹೈದರಾಬಾದ್  
आई आई टी हैदराबाद  
IIT Hyderabad



**Chips to Startup  
Programme**  
An initiative by Ministry of Electronic  
and IT, Government of India

**SYNOPSYS**<sup>®</sup>  
*Silicon to Software*<sup>™</sup>



VLSI System Design

# Cloud based Analog IC Design Hackathon

This is to certify that

*ULLAM KAVYA*

from

*National Institute of Technology Goa*

has designed the circuit

*Curvature Compensated CMOS Band Gap Reference Circuit*

using Synopsys Custom Compiler Platform

She/He has performed an **Outstanding** ~~Excellent/Very Good/Good~~ work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.

**Dr Ashudeb Dutta**  
Program Co-Ordinator, IIT Hyderabad

**Dr B Umashankar**  
Chair CCE, IIT Hyderabad