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आई आई टी हैदराबाद
IIT Hyderabad



**Chips to Startup
Programme**
An initiative by Ministry of Electronic
and IT, Government of India

SYNOPSYS[®]
Silicon to Software™



VLSI System Design

Cloud based Analog IC Design Hackathon

This is to certify that

Salai Pragadeshwaran B

from

National Institute of Technology Trichy

has designed the circuit

Four-Quadrant CMOS Analog Multiplier

using Synopsys Custom Compiler Platform

She/He has performed an **Outstanding** ~~Excellent/Very Good/Good~~ work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.

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