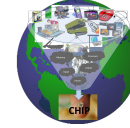




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IIT Hyderabad



SYNOPSYS[®]
Silicon to Software™



VLSI System Design

Cloud based Analog IC Design Hackathon

This is to certify that

Runal Kumar Panja

from

Institute of Radio Physics and Electronics

has designed the circuit

Current Controlled Tunable Bandwidth Low Pass Filter using 3-MOS Model

using Synopsys Custom Compiler Platform

She/He has performed an ~~Outstanding/Excellent/Very Good~~ **Good** work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.

Dr Ashudeb Dutta
Program Co-Ordinator, IIT Hyderabad

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