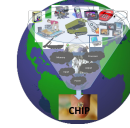




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IIT Hyderabad



SYNOPSYS[®]
Silicon to Software™



VLSI System Design

Cloud based Analog IC Design Hackathon

This is to certify that

Dr. Subhash C. Arya

from

North Eastern Hill University, Shillong

has designed the circuit

Driver Circuit of MZI Modulator for PICs

using Synopsys Custom Compiler Platform

She/He has performed an ~~Outstanding~~ **Excellent** ~~Very Good~~ ~~Good~~ work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.

Dr Ashudeb Dutta
Program Co-Ordinator, IIT Hyderabad

Dr B Umashankar
Chair CCE, IIT Hyderabad