

Cloud based Analog IC Design Hackathon

This is to certify that

Aruna Kumara Sarma K

from

HCL Technologies, Bengaluru, K.A

has designed the circuit

Low Power Large Range Bi-directional Level Shifter

using Synopsys Custom Compiler Platform

She/He has performed an ~~Outstanding~~/**Excellent**/~~Very Good~~/~~Good~~ work.

This program was conducted between 15 February - 1 March 2022 as an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt.



Dr Ashudeb Dutta
Program Co-Ordinator, IIT Hyderabad



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